

# **REVOX C-27X C270 TC - C274 - C278 TLS-4000**

## **INTERFACEDOCUMENTATION**

**Interface number : 1.812.404.20  
IF - Doc number : 10.27.1620**

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Order No: 10.27.1620 (Ed. 0790)

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### 3 Operating Instructions

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#### 3.1 Technical Specifications

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- Slave type:
  - SMPTE/EBU - TC machine with move pulse information
  - No code during wind modes
  - GOTO function with PLAY - STOP sequenze
  - Park in LOCK with delay.
  - CHASE-PLAY with delay.
- Link type:  
parallel (PLAY, STOP, FWD, RWD, REC)
- Capstan control:  
frequency 9600 Hz nominal
- Movepulse information:  
Clock and direction.  
(frequency see 3.4)
- Lock time (typ):  
(in CUED status, Master Start - SYNC): 2 sec  
(in CHASE 10\*vnom, Master Start - SYNC): 10 sec
- Drop-In Delay:  
Drop-Out Delay: compensated  
compensated
- TC Compensation:  
C270TC with slave internal compensation  
C274/C278 not necessary
- Wow & Flutter:  
less then 20% higher than the Wow & Flutter of the slave  
(normaly within the slave rates)

### 3.2 Summary of Supported Functions

#### Operating conditions:

- STOP
- PLAY, REC nominal (internal reference or ext. varispeed, TLS reference  $\pm 50\%$  )
- EDIT same as STOP
- FORW, REW Variable continuously from 0 to .. vmax
- SHTLF, SHTLR same as FORW,REW
- LOC, LOCREL  
made by interface
- MUTE  
not implemented
- REHEARSE  
available with PLAY instead of REC  
(see 3.3 DIL-SWITCH)
- EVENT RELAY  
There is a relay available for the user. The relay can be switched on by the EVON synchronizer command and off by the EVOFF command. (see 3.4)
- CONDITIONAL COMMANDS  
The execution of synchronizer commands can be submitted at certains events.
- STATUS REQUEST  
The status information are requested and updated through the parallel communication link by the interface software.  
Additional information on the nominal speed is available at the synchronizer via the move pulse connection.  
The C270 TC does not have a tapeout status. Instead of this the C270 TC answers with a STOP status.
- Audio-, TC channel set up  
not implemented.
- TRANSPARENT Commands  
not implemented.
- KEYBOARD DISABLE  
Not implemented.

### 3.3 DIL-SWITCH Functions

The following functions are given to the DIL SWITCH SZ81:

- switch 1: Active polarity of the RECEN signal.  
OFF : recording mode enabled with RECEN LOW  
ON : recording enabled with RECEN HIGH or open
- switch 2: Rehearsal mode  
If the rehearsal mode is active, RECORD commands will be  
OFF : replaced by PLAY commands  
ON : directly passed to the slave

This switch will be off when there is no external REHEARSAL circuit (controled by the signal B-REHR at SLAVE CONTROL B). So you can be shure, than no RECORD command will be given to the slave during the rehearsal mode.

The position of the DIL-SWITCH will only be checked just after switching on the rehearsal mode. If you change the position of this switch, it is necessary to switch on the rehearsal mode one more time.

- All other switches are not used, but they should be in off position to guaranty compatibility with later software versions.
- Default Settings:  
all switches in OFF position

### 3.4 Additional Features at Slave Control B Connector

RECEN	(PIN 2): Hardware record enable. The function of this input are ruled by the DIL-SWITCH 1. (Ref to section 3.3)
REL1	(PIN6), REL2 (PIN7): This relay contact can be used for any general purpose. It has to be turned ON and OFF by the EVON and EVOFF synchronizer commands.
B-REHR	(PIN15): Output of the rehearse status (open collector, active low). It is switched active by the RHRSON synchronizer command.
SREHSL	(PIN 12): TTL compatible input for switching on the rehearsal mode. HIGH (OR OPEN): REHEARSAL OFF LOW: REHEARSAL ON

**MVCL****(PIN21), MVDR (PIN24):**

This output (open collector) provides out the slave movepulse information for external use.

MVCL: The frequency depends on the nominal speed

MVDR: LOW = forward

3.75 ips : 8 Hz

7.5 ips : 16 Hz

15 ips : 32 Hz

**XVSEN/REF****(PIN 5, PIN 3):**

An external varispeed circuit can be connected to the slave control B connector. The two signals are switched to the slave during the OFF-mode of the synchronizer.

PAIN 9

LOW = varispeed enable

PAIN 10 reference frequency

(nominal 9600 Hz)

### 3.5 LED Diagnostic Display

Three LEDs are available as error condition display and status indicators of the front part of the interface.

#### DL 1 2 3 (Front view)

- An initialization procedure is executed after reset and the main hardware devices are tested. If an error condition occurs the left LED (DL1) will blink (ca 1 hz) and the other two LED will indicate the error code.

DL 1 2 3 (# = LED blinking, - = LED off, \* = LED on)

DL1	DL2	DL3	
#	-	-	CPU RAM test failed.
#	-	*	RAM test failed.
#	*	-	SSDA test failed.

- After the program initialization the left LED must be off and the other two right LEDs are used as status indicator in this way:

#### DL 1 2 3

DL1	DL2	DL3	
-	*	*	no connection with the synchronizer board
-	#	-	no connection with the SLAVE

- If the left LED is on, a fatal processor error has occurred. The following list shows the error codes. A reset is necessary to return to operation mode.

DL1	DL2	DL3	
*	-	-	Fatal soft- or hardware error (ev. ROM defect)
*	-	*	Watch dog error
*	*	-	Clock error
*	*	*	Illegal opcode
#	#	#	Microprocessor 68HC11 has to be reconfigured

### 3.6 Test points

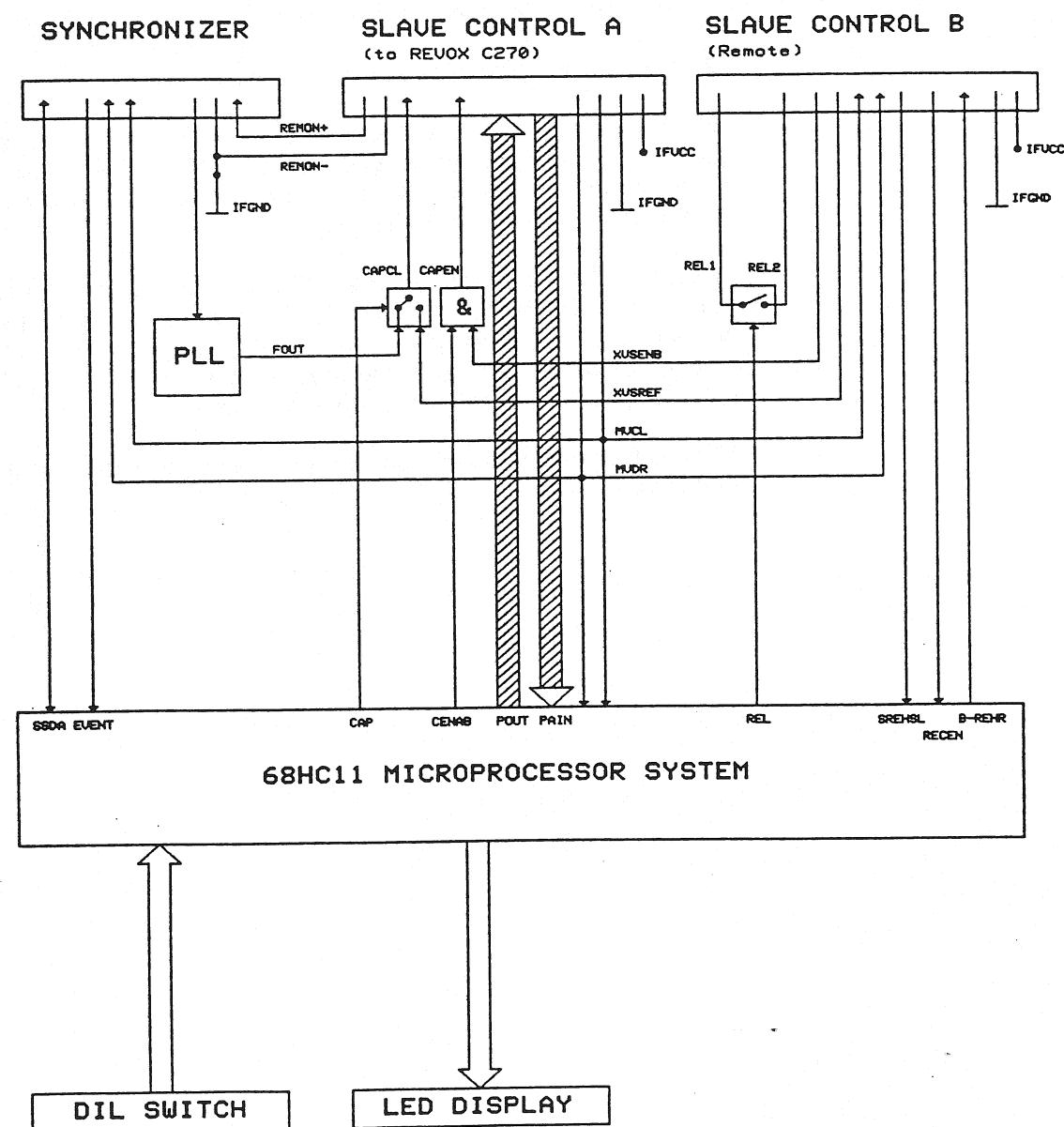
None

### 3.7 Applications hints

- Use move pulse information as master tallies ( see 2.2 )
- With a C270 TC it is recommended to run with 7.5 or 15 ips, because of the poor time code quality provided by the internal time code processor. There are no restrictions with the C274 or C278, because time code is recorded on any of the audio tracks.
- Since the C270 TC doesn't provide a 'Tapeout' status, the interface can not recognize it and reads a 'STOP'. If a 'Tapeout' occurs during locating or chasing, it is recommended to switch the synchronizer to OFF before loading the tape again. After the new loading put the slave to 'PLAY', so that the synchronizer can read a good time code.
- The interface can be used to support an external rehearsal circuit. (see 3.4 and 3.5)

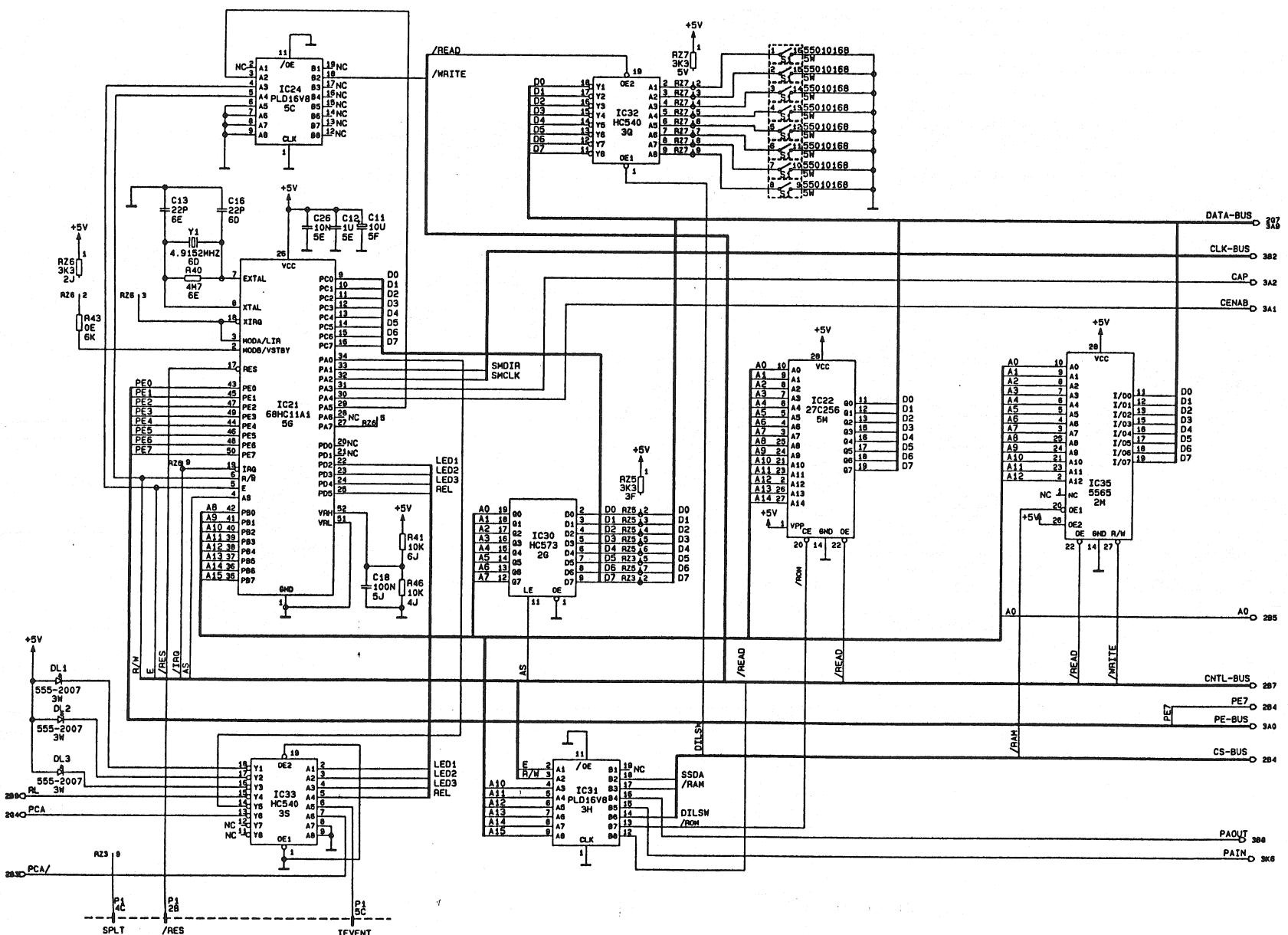
## 4 Service documentation

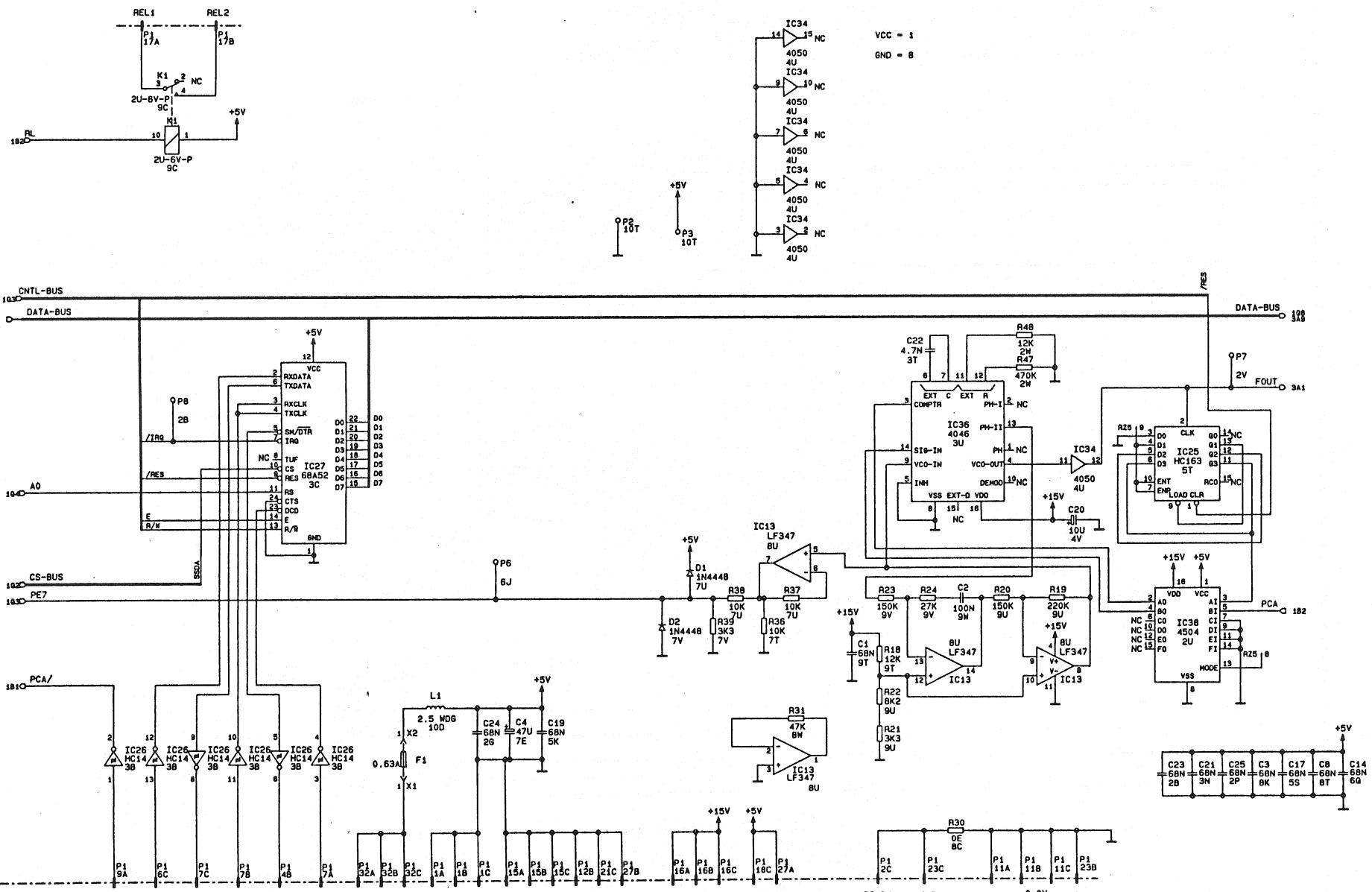
### 4.1 Block diagram



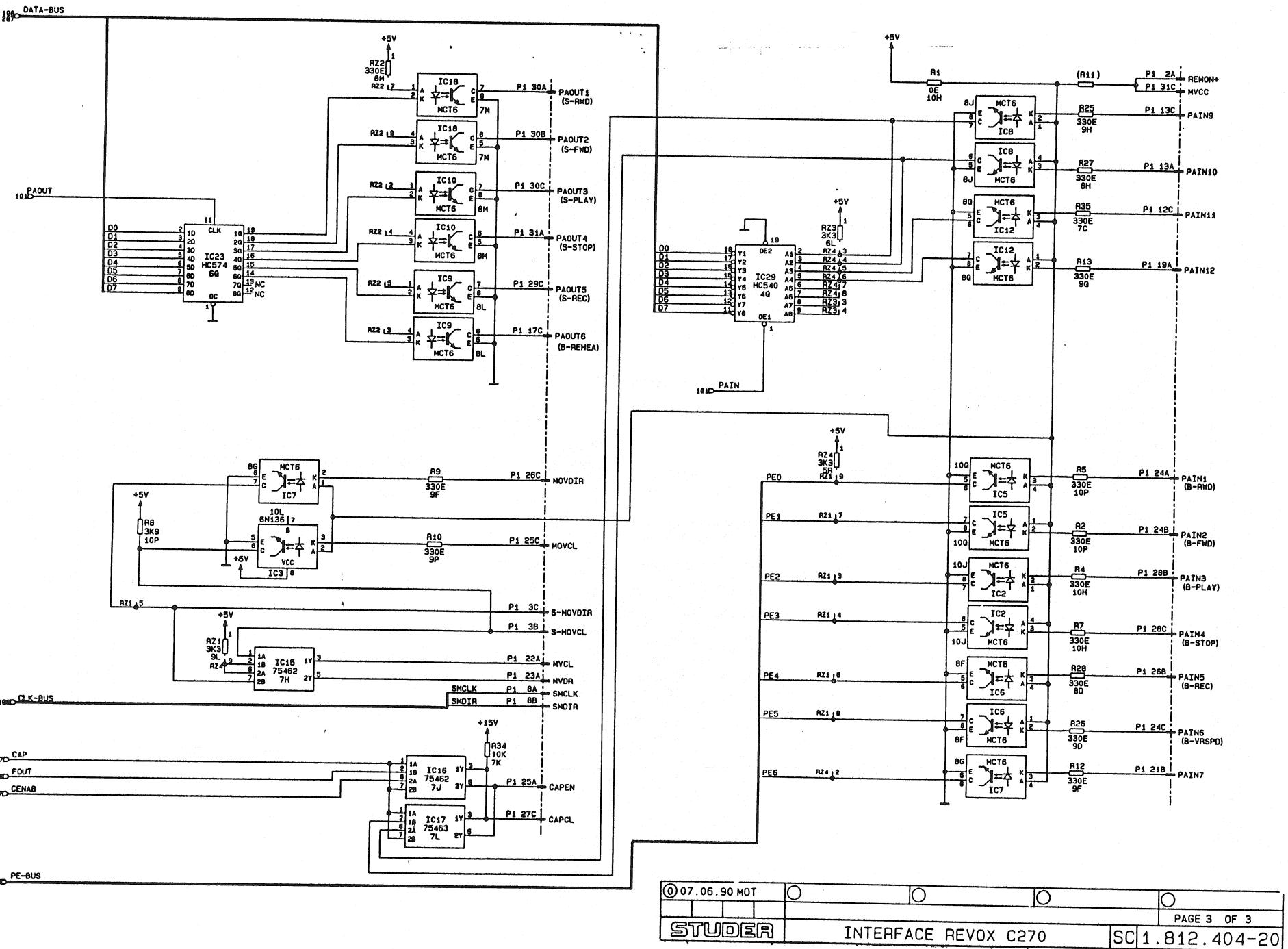
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TLS 4000 MK2			PAGE 1 OF 1	
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## 4.2 Diagrams

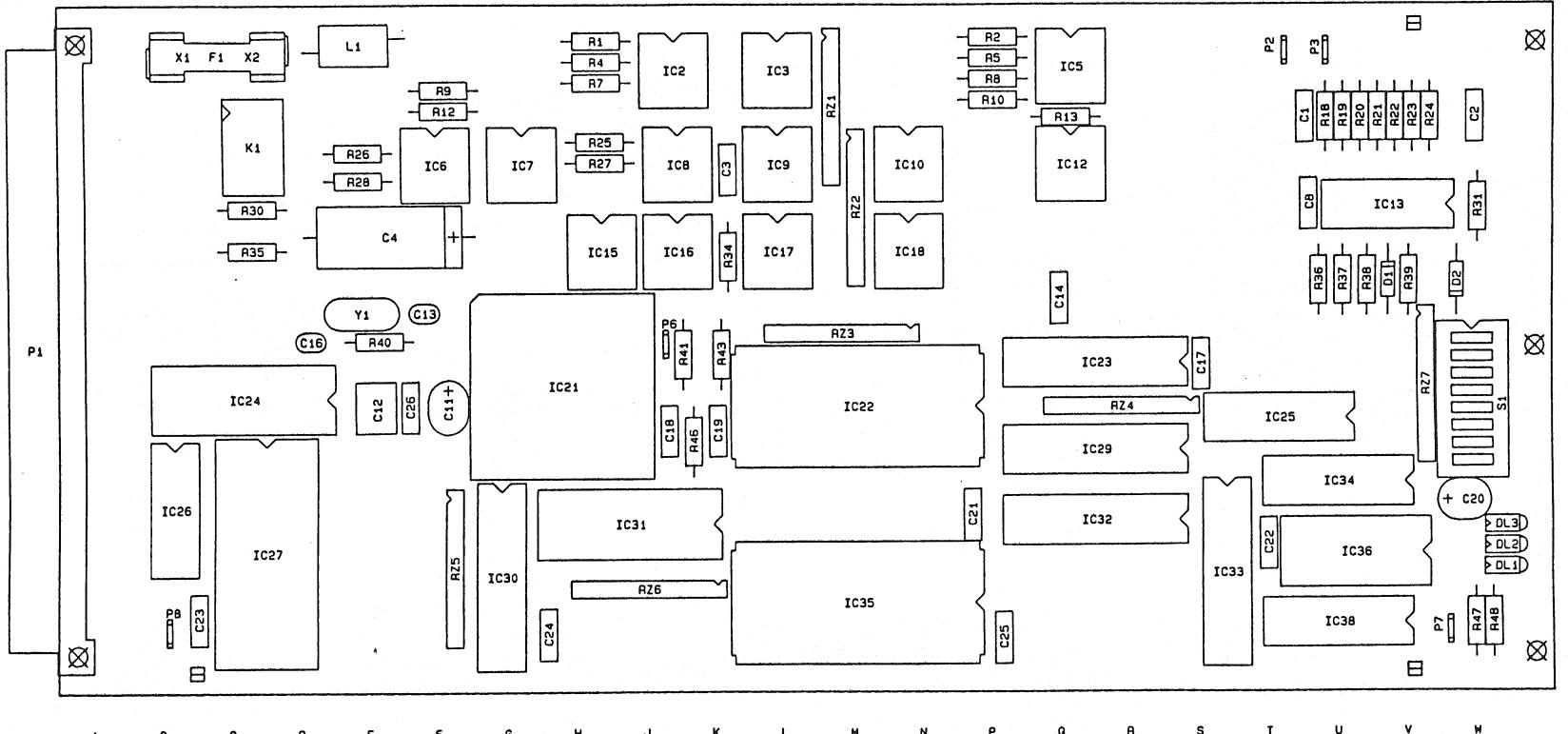




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### 4.3 Component arrangement



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## 4.5 Signal description, slave connectors

### SLAVE CONTROL A:

Pin	Signal	Type	Description
1	MGND		0.0 V
2	B-RWD		Rewind status
3	B-FWD		Forward wind status
4	B-VRSPD		(not used, cause only in C274/78 impl.)
5	CAPEN	o.c. out	capstan varispeed enable (LOW = enb)
6	RX/RA	RS232 in	(not used)
7	MOVCLK	a.l. input	move signal clock from C270
8	TX/TA	RS232 out	(not used)
9	B-REC		Record status
10	MOVDIR	a.l. input	move signal direction from C270 LOW = rewind
11	IFVCC		(not used)
12	IFGND		(not used)
13	CAPCL	o.c. out	capstan clock (9600Hz nominal)
14	RB		0.0 V (not used)
15	B-PLAY		PLAY status
16	B-STOP		STOP status
17	S-MUTE		(not used, cause it's not impl.)
18	S-LIFT		(not used, cause it's not impl.)
19	S-RECORD		Record command
20	S-REWIND		Rewind command
21	S-FORWARD		Forward wind command
22	S-PLAY		PLAY command
23	S-STOP		STOP command
24	TB		(not used)
25	MVCC		+ 24 V

signal types:

o.c. out

open collector output, max 30V/0.03A  
(no internal pullup resistor)a.l.  
inputactiv low  
optoisolator input, driven by open collector, or  
by a switch to GND. (activ: > 10 mA)

## SLAVE CONTROL B:

Pin	Signal	Type	Description
1	IFGND		0.0 V
2	RECEN/PAIN11	a.l. input	record enable / safe input (see DIL-SWITCH 81.1)
3	XVSREF/PAIN10		external varispeed frequency (not used)
4	BVRSPD		
5	XVSENBL/PAIN9	a.l. input	external varispeed enable
6	REL1	current lp.	event relay contact 100V/0.3A
7	REL2	current lp.	event relay contact 100V/0.3A
8	PAOUT6	o.c. out	rehearsal on indication
9	PAOUT7		
10	PAOUT8		
11	IFVCC		(not used)
12	SREHSL/PAIN12	a.l. input	rehearsal input
13	PAIN13	a.l. input	(not used)
14	DC		(not used)
15			(not used)
16	PAIN14		(not used)
17	PAIN15		(not used)
18	PAIN16		(not used)
19	PAIN17		(not used)
20	IFGND		(not used)
21	MVCL	o.c. out	move signal clock (9600Hz nominal)
22	SCITX		
23	SCIRX		
24	MVDR	o.c. out	move signal direction (LOW = REW)
25	IFGND		(not used)

signal types:

o.c. out

open collector output, max 30V/0.03A  
(no internal pullup resistor)a.l.  
inputactiv low  
optoisolator input, driven by open collector,  
or by a switch to GND. (activ: > 10 mA)

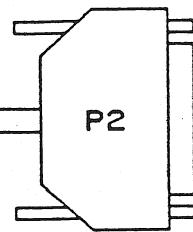
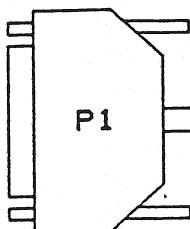
current lp.

current loop, max 24V AC/DC

## 4.6 IF Cable Description

TLS 4000 MK2  
SLAVE CONTROL A

REVOX C27X



P1. 1		MGND	P2. 1
2		PAIN1	2
3		PAIN2	3
5		CAPEN	5
7		MOUCL	7
9		PAINS	9
10		MOUDIR	10
13		CAPCL	13
15		PAIN3	15
16		PAIN4	16
19		PAOUT5	19
20		PAOUT1	20
21		PAOUT2	21
22		PAOUT3	22
23		PAOUT4	23
25		MUCC	25
12		SCREEN	

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